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PTO/SB/21 (01-03)  
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<b>TRANSMITTAL FORM</b> <i>(to be used for all correspondence after initial filing)</i>  <b>Mail Stop: AMENDMENT</b>  Express Mail Receipt No.  Total Number of Pages in This Submission	Application / Conf. No.	10/824,675 / 8082
	Filing Date	April 14, 2004
	First Named Inventor	Robert O. Conn
	Examiner Name	Donghee Kang
	Art Unit	2811
	Patent No.	
Attorney Docket Number		X-1322-1-1D US

ENCLOSURES (check all that apply)		
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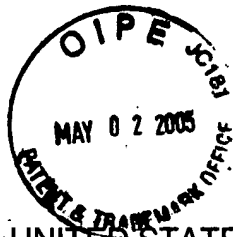
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X-1322-1-1D US

10/824,675



PATENT

Conf. No. 8082

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Robert O. Conn

Assignee: Xilinx, Inc.

Title: Bond and Back Side Etchback Transistor Fabrication Process

Serial No.: 10/824,675

Filing Date: 04/14/2004

Examiner: Donghee Kang

Art Unit: 2811

Docket No.: X-1322-1-1D US

Conf. No.: 8082

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AMENDMENT IN RESPONSE TO THE FIRST OFFICE ACTION

In response to the First Office Action mailed from the Patent Office on February 3, 2005, Applicant submits the following amendments and remarks.

**Amendments to the Claims** begin on page 2 of this paper.

**Remarks** begin on page 5 of this paper.